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APPLICATION NO.	FI	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/826,240	04/04/2001		Anil Kavipurapu	KAVIPURAPU I	4102
47396	7590	01/10/2005		EXAMINER	
HITT GAII AGERE SY	•	IC.	YANCHUS III, PAUL B		
PO BOX 83			ART UNIT	PAPER NUMBER	
RICHARDS	ON, TX	75083	2116		

DATE MAILED: 01/10/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
Advisory Action	09/826,240	KAVIPURAPU, ANIL				
navicely neutrin	Examin r	Art Unit				
	Paul B Yanchus	2116				
The MAILING DATE of this communication appe	ars on the cover sh t with the c	orrespondence address				
THE REPLY FILED 12/10/2004 FAILS TO PLACE THIS Therefore, further action by the applicant is required to a final rejection under 37 CFR 1.113 may only be either: (condition for allowance; (2) a timely filed Notice of Appel Examination (RCE) in compliance with 37 CFR 1.114.	void abandonment of this application to the same of th	cation. A proper reply to a ch places the application in				
PERIOD FOR RE	PLY [check either a) or b)]					
a) The period for reply expiresmonths from the mailing of the period for reply expires on: (1) the mailing date of this Advevent, however, will the statutory period for reply expire later the ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f).  Extensions of time may be obtained under 37 CFR 1.136(a). The data	risory Action, or (2) the date set forth in th an SIX MONTHS from the mailing date o FILED WITHIN TWO MONTHS OF THI	f the final rejection. E FINAL REJECTION. See MPEP				
have been filed is the date for purposes of determining the period of exten 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened (b) above, if checked. Any reply received by the Office later than three moderned patent term adjustment. See 37 CFR 1.704(b).	sion and the corresponding amount of the I statutory period for reply originally set in onths after the mailing date of the final reje	efee. The appropriate extension fee under the final Office action; or (2) as set forth in action, even if timely filed, may reduce any				
1. A Notice of Appeal was filed on Appellant' 37 CFR 1.192(a), or any extension thereof (37 CF						
2. The proposed amendment(s) will not be entered b	ecause:					
(a) They raise new issues that would require further consideration and/or search (see NOTE below);						
(b) they raise the issue of new matter (see Note I						
(c) they are not deemed to place the application issues for appeal; and/or	in better form for appeal by mat	erially reducing or simplifying the				
(d) they present additional claims without cancel NOTE:	ling a corresponding number of	finally rejected claims.				
3. Applicant's reply has overcome the following reject	ction(s):	·				
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	I be allowable if submitted in a s	separate, timely filed amendment				
5 ☒ The a)☐ affidavit, b)☐ exhibit, or c)☒ request for application in condition for allowance because: See		sidered but does NOT place the				
6. The affidavit or exhibit will NOT be considered be raised by the Examiner in the final rejection.	cause it is not directed SOLELY	to issues which were newly				
7. For purposes of Appeal, the proposed amendmen explanation of how the new or amended claims w						
The status of the claim(s) is (or will be) as follows:	•					
Claim(s) allowed:						
Claim(s) objected to:						
Claim(s) rejected:						
Claim(s) withdrawn from consideration:						
8. The drawing correction filed on is a) app	proved or b) disapproved by	the Examiner.				
9.⊠ Note the attached Information Disclosure Statement(s)( PTO-1449) Paper No(s). <u>12/17/2004</u> .						
10. Other:	•	2				
		LYNNE H. BROWNE RVISORY PATENT EXAMINER CHNOLOGY CENTER 2100				

Continuation of 5. does NOT place the application in condition for allowance because: The applicants argue that Atkinson does not disclose an alternative power mode that is obtained by adapting a reconfigurable circuit. However, Atkinson discloses that the CPU chip executes at a clock speed which is indicated by the CPUWRMODE register. The CPUWRMODE register is located in the CPU chip [column 4, lines 19-31]. The execution speed of the CPU chip is reconfigured by adapting the CPUWRMODE register. Therefore, Atkinson does diclose an alternate power mode that is obtained by adapting a reconfigurable circuit.